

ABSTRACT

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In a circuit driving a capacitive load C_p , current passed through a transistor Q_3 , a diode D_1 and a recovering coil L is passed through lines L_1 , L_2 , and the inductance components of the lines L_1 and L_2 , and the drain-source capacitances of the transistors Q_1 and Q_2 generate LC resonance. Capacitors C_1 and C_2 are connected in parallel to the drain-source regions of the transistors Q_1 and Q_2 to increase the total drain-source capacitance and reduce the resonance frequency, so that unwanted electromagnetic wave radiation in a frequency band affecting other electronic devices is suppressed.